

The Summit brought together a diverse mix of government leaders, policymakers, and technology innovators, creating space for practical, experience-driven discussions on how digital technologies ...

**Abstract** We demonstrate a low-power (1 pJ/bit), C-band 4x56 Gbit/s NRZ optical receiver constructed from a 28nm CMOS transimpedance amplifier and a Silicon PIC containing a Ge photodetector array.

Here we present for the first time the integration of lithium tantalate onto a silicon photonics chip. This integration is achieved without modifying the standard silicon photonics process...

As a reminder, our portfolio is highly diversified, supporting NRZ, PAM4 and coherent modulations across EML, silicon photonics and VCSEL-based architectures.

The technology provides high-quality active and passive silicon photonics devices with high-ft CMOS transistors by employing dual silicon thickness and two contact modules CA and CB.

(NRZ) can further improve the bandwidth of optical and wireline links by relaxing the bandwidth requirements for the ORX front-end and reducing the complexity of the clock generation and ...

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osstalk penalties, unlocking the design space for ultra-broadband Kerr comb-driven DWDM links. In this study, we present our latest design and characterization of a SiPh microresonator-based DWDM ...

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